STEFANOS **ELEFTHERIADIS**

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OBJECTIVE

PhD Candidate in Electrical Engineering with a focus on Sustainable Power Systems. Strong academic foundation in hardware design, embedded systems, and secure computing. Passionate about advancing sustainable energy technologies through innovative research and interdisciplinary collaboration. Committed to developing efficient, reliable, and environmentally conscious power solutions for the future.



EDUCATION

PhD in Electrical Engineering | Cyprus University of Technology SINCE SEPTEMBER 2025 –

Research Focus: Sustainable Power Systems and Smart Grid Technologies

Electrical Engineering (bachelor's degree) | Cyprus University of Technology SEPTEMBER 2021 – 2025

Graduated with an average grade of 8.786

Thesis: "Hardware Design and Implementation of Hash Functions with Embedded Scan-Based Techniques"

Digital Technology and Programming (High School Diploma) | B' Technical and Vocational School of Education and Training Grigoris Afxentiou
SEPTEMBER 2018 – JUNE 2021



EXPERIENCE

Computer Engineering Assistant | V.D. Computerland Center LTDJUNE 2018 – JULY 2019

Different jobs that I have experienced: Repairing computers, Networking of a company, Analyse digital signals and networks with the use of network analysers and digital multimeters, Install security systems, Continually served as a helpful and enthusiastic assistant with a proven track record of doing work in a timely and professional manner. Served as a direct assistant to Production Supervisor, supporting all aspects of production and office management.

Teacher of student tutoring support | Cyprus University of Technology SEPTEMBER 2023 – DECEMBER 2023

Tutoring Bachelor students in the Digital Systems Course.

External Associate in "Research topics in Electrical Engineering and Energy" | Cyprus University of Technology

OCTOBER 2024 - NOVEMBER 2024

The work involves providing support to power systems research under the guidance of the Research Supervisor Assistant Professor.

Teacher Assistant of Digital System Lab | Cyprus University of Technology JANUARY 2025 – APRIL 2025

Assisted undergraduate students in grasping core concepts of digital logic design, circuit simulation, and FPGA implementation. Provided hands-on guidance during lab exercises and helped clarify technical challenges. Collaborated with the course instructor to improve lab materials and ensure the effective use and maintenance of lab equipment.



- Proficient with Coding Languages such as C, C++, MATLAB, python
- Speak English Fluently

- Proficient with Hardware Designing Languages (VHDL, Verilog HDL)
- Advanced skills in the use of microcontrollers such as Arduino



ACTIVITIES

Sport (Basket, Football, Martial arts), Music (playing the piano), Coding and designing embedded systems.