



Cyprus
University of
Technology

EEN442 - Power Systems II (Συστήματα Ισχύος II)

Part 5: Fault analysis

<https://sps.cut.ac.cy/courses/een442/>

Dr Petros Aristidou

Department of Electrical Engineering, Computer Engineering & Informatics

Last updated: October 2, 2023


After this part of the lecture and additional reading, you should be able to . . .

- 1 . . . explain the common types of faults occurring in electric power systems;
- 2 . . . perform fault analysis in simple power systems analytically;
- 3 . . . use computational tools to perform fault analysis in more complex systems.

- 1 **Overview**
- 2 **Fault at RL circuit**
- 3 **Short circuit of a synchronous machine**
- 4 **Short-circuit capacity (or fault level)**
- 5 **Algorithms for short circuit studies**

- 1 Overview**
- 2 Fault at RL circuit
- 3 Short circuit of a synchronous machine
- 4 Short-circuit capacity (or fault level)
- 5 Algorithms for short circuit studies

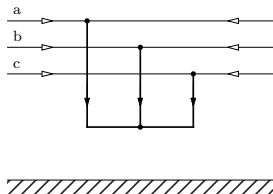
- A fault in an electrical power system is the unintentional and undesirable creation of a conducting path (a **short circuit**) or a blockage of current (an **open circuit**)
- This can happen through insulation failure of equipment or flashover of lines initiated by a lightning stroke or through accidental faulty operation
- Depending on the location, the type, the duration, and the system grounding short circuits may lead to:
 - electromagnetic interference with conductors in the vicinity (disturbance of communication lines)
 - stability problems
 - mechanical and thermal stress (i.e. damage of equipment, personal danger)
 - danger for personnel

 <https://youtu.be/nTWc45JRQGs>

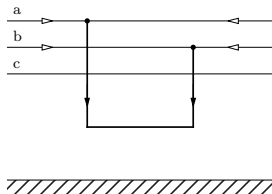
- In high voltage networks, **short circuits** are the most frequent type of faults.
- The system must be **protected** against flow of heavy short circuit currents by disconnecting the faulty part of the system by means of circuit breakers operated by protective relaying
- The safe disconnection can only be guaranteed if the current does not exceed the capability of the circuit breaker
- The short circuit currents in the network must be computed and compared with the ratings of the circuit breakers at regular intervals as part of the normal operation planning

1 Types of short circuits

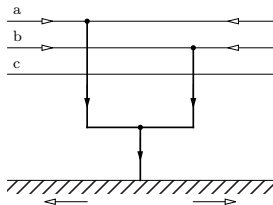
a) Symmetrical three-phase short circuit



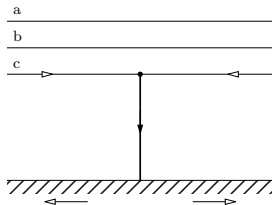
b) Two-phase without ground contact



c) Two-phase with ground contact



d) Single-phase earth fault



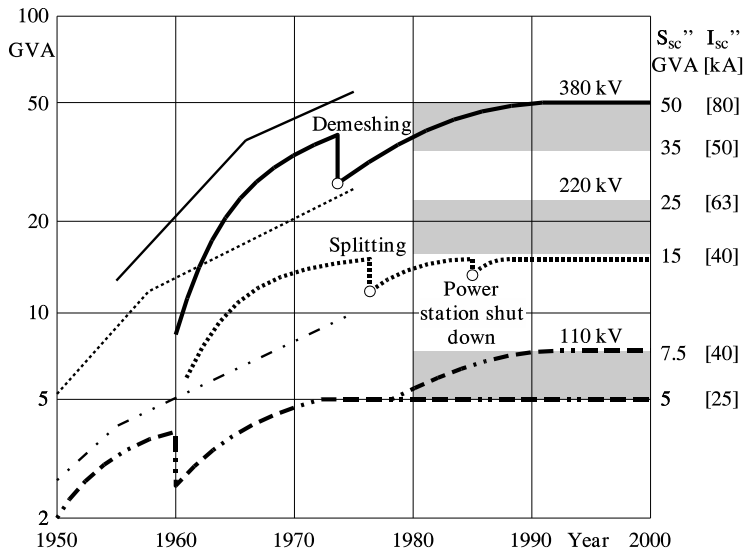
→ SC current

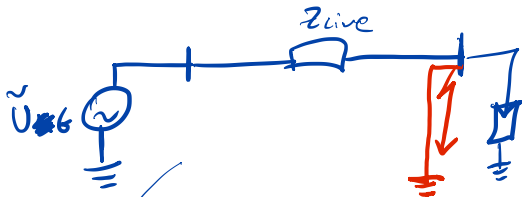
→ partial SC current in
conductor or ground

- Short circuits may be solid or may involve an arc impedance.
- The most frequent type of faults are single-phase earth faults, which typically constitute 50 - 80 % of all faults on transmission lines.
- Number of faults vary from region to region and depends on meteorological conditions, e.g. lightning intensity, and other factors.
- In Germany and Switzerland, faults occur with a frequency of 2-5 faults per year and 100 km in the transmission systems.
- In Cyprus, faults occur with a frequency of 1.5-3 faults per year and 100 km in the transmission systems (66kV and above).

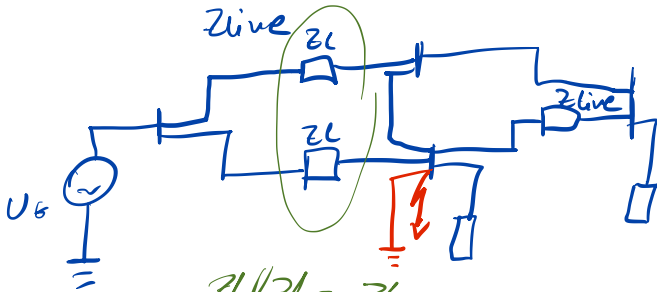
- The majority of system faults **are not** three-phase faults but faults involving one line to ground or occasionally two lines to ground. These are unsymmetrical faults requiring special computational methods like symmetrical components (see previous part of EEN442).
- Symmetrical faults are rare but they lead to the most severe fault current flow against which the system must be protected. They are also simpler to carry out.

1 Development of short circuit currents over the years





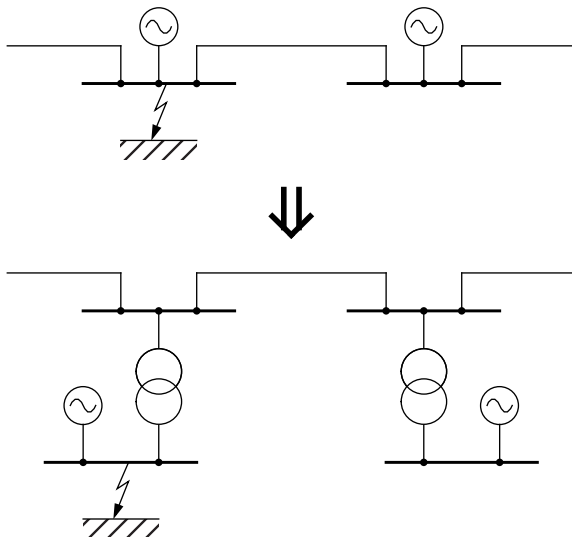
$$I_{sc} = \frac{U_G}{Z_{line}}$$



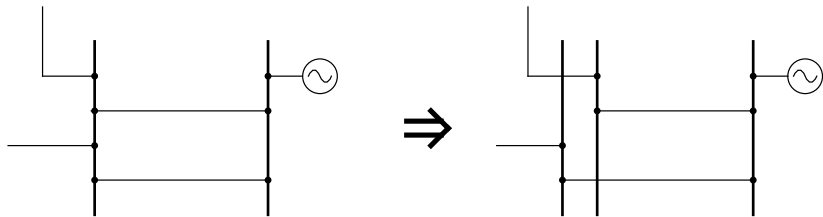
$$Z_L // Z_L = \frac{Z_L}{2}$$

- The short circuit currents at network nodes are generally increasing over the years due to
 - more generators,
 - new lines in existing networks,
 - interconnection of isolated networks to an integrated one.
- This is primarily a problem for the *expansion planning*, where the impacts of long-term changes on the short circuit currents have to be assessed.
- If the short circuit current exceeds the admissible limit at a network node, the circuit breakers have to be replaced by breakers with higher ratings (**very expensive**)
- Alternatively, the impedance between feeder and fault location can be increased in order to reduce the short circuit current. This is commonly achieved by
 - ① introducing a higher voltage level while splitting the existing lower voltage network,
 - ② use of multiple busbars,
 - ③ fast decoupling of busbars during faults.

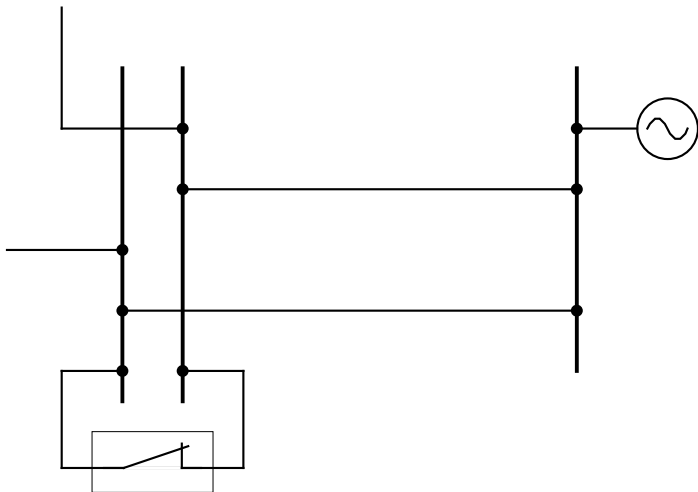
1 Introduction of a higher voltage level



1 Multiple busbar operation



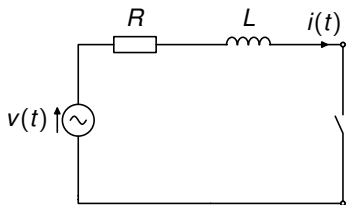
1 Fast busbar decoupling



$$t_s = 70 \dots 80\text{ms}$$

- 1 Overview
- 2 Fault at RL circuit**
- 3 Short circuit of a synchronous machine
- 4 Short-circuit capacity (or fault level)
- 5 Algorithms for short circuit studies

Assume an RL circuit (e.g., the equivalent model of a transmission line) fed by an AC voltage source:



where $v(t) = \sqrt{2}U \sin(\omega t + \alpha)$.

- A short circuit is assumed to take place at $t = 0$. The parameter α controls the instant on the voltage wave when short circuit occurs.
- It is known from circuit theory that the current after short circuit is composed of two parts, i.e.

$$i = i_s + i_t$$

- Assuming the RL complex impedance \underline{Z} :

$$\underline{Z} = \sqrt{R^2 + \omega^2 L^2} \angle \left(\theta = \tan^{-1} \frac{\omega L}{R} \right).$$

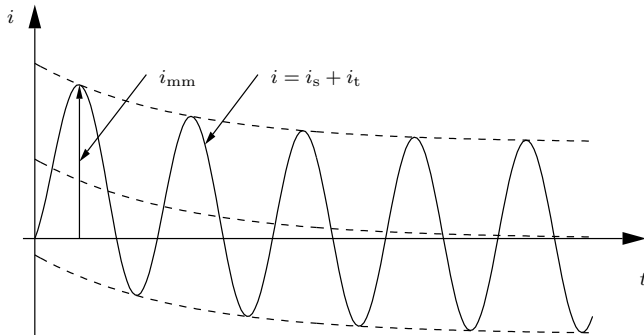
- i_s represents the steady state alternating current:

$$i_s = \frac{\sqrt{2}U}{|Z|} \sin(\omega t + \alpha - \theta)$$

- i_t represents the transient direct current:

$$i_t = -i_s(0) e^{-(R/L)t} = \frac{\sqrt{2}U}{|Z|} \sin(\theta - \alpha) e^{-(R/L)t}$$

Plotting the two gives:



- the sinusoidal steady state current is called the ***symmetrical short circuit current***
- the unidirectional transient component is called the ***DC off-set current***
- the total short circuit current to be unsymmetrical until the transient decays

- The maximum momentary current that a circuit breaker would need to break at the first peak can be characterized as:

$$i_{mm} = \frac{\sqrt{2}U}{|Z|} \sin(\theta - \alpha) + \frac{\sqrt{2}U}{|Z|}$$

- Assuming the transmission line resistance is small ($\theta \approx 90^\circ$):

$$i_{mm} = \frac{\sqrt{2}U}{|Z|} \cos \alpha + \frac{\sqrt{2}U}{|Z|}$$

- The maximum possible value is for $\alpha = 0$, i.e. the short circuit occurring when the voltage wave is going through zero. Thus, i_{mm} may be as high as twice the maximum of the symmetrical short circuit current:

$$i_{mm} \leq 2 \frac{\sqrt{2}U}{|Z|}$$

- For the selection of circuit breakers, momentary short circuit current is taken corresponding to its maximum possible value.

- Modern circuit breakers are designed to interrupt the current in the first few cycles (five cycles or less)
- When the current is interrupted, the DC off-set i_t has not yet died out and contributes thus to the current to be interrupted
- Computing the value of the DC offset at the time of interruption would be highly complex in a network of even moderately large size. Thus, we:
 - ① calculate the initial symmetrical short-circuit current alone using the methods detailed later on
 - ② apply an empirical multiplying factor to account for the DC off-set current

Some of the relevant regulations to account for the DC offset:

- UK Engineering Recommendation ER G7/4:

$$I_{\text{peak}} \cong \begin{cases} 1.8\sqrt{2}I''_{\text{sc}} & \text{for low voltage systems } (< 1\text{ kV}) \\ 2\sqrt{2}I''_{\text{sc}} & \text{for high voltage systems } (> 1\text{ kV}) \end{cases}$$

- IEC 909:

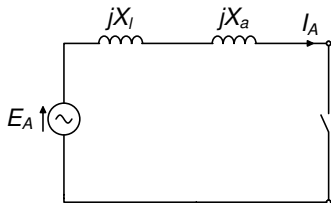
$$I_{\text{peak}} \cong \sqrt{2}I''_{\text{sc}}(1.02 + 0.98e^{-3(R/X)})$$

where in radial systems R/X is the ratio of the equivalent short-circuit impedance at the fault location. In meshed systems, it can be:

- Method A: Uniform Ratio R/X
- Method B: R/X ratio at short-circuit location
- Method C: Equivalent frequency

- 1 Overview
- 2 Fault at RL circuit
- 3 Short circuit of a synchronous machine**
- 4 Short-circuit capacity (or fault level)
- 5 Algorithms for short circuit studies

As seen in the previous chapters, the synchronous machine model in steady-state as:

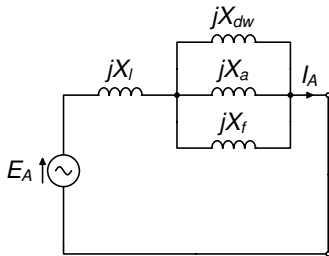


where X_l is the leakage reactance and X_a the armature reactance. Together, they form the **synchronous reactance** $X_d = X_a + X_l$.

- In the event of a short circuit, the symmetrical short circuit current is limited initially only by the leakage reactance of the machine.
- Since the air gap flux cannot change instantaneously, to counter the demagnetization of the armature short circuit current, currents appear in the field winding as well as in the damper winding in a direction to help the main flux.
- These currents decay in accordance with the winding time constants. The time constant of the damper winding which has low X/R -ratio is much less than the one of the field winding, which has high leakage inductance with low resistance.

3 Synchronous reactance during short circuit

- Thus, during the initial part of the short circuit, the damper and field windings have transformer currents induced in them.
- In the circuit model their reactances— X_f of field winding and X_{dw} of damper winding—appear in parallel with X_a as shown below

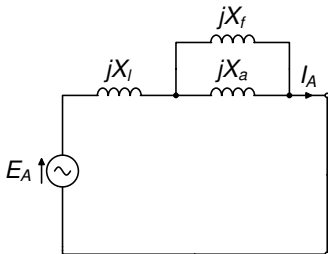


- The total reactance is called **sub-transient reactance**:

$$X_d'' = X_l + \frac{1}{1/X_a + 1/X_f + 1/X_{dw}} \quad (3.1)$$

3 Synchronous reactance during short circuit

- As the damper winding currents are first to die out, X_{dw} effectively becomes open circuited.
- The machine reactance thus changes from the parallel combination of X_a , X_f , and X_{dw} during the initial period of the short circuit to X_a and X_f in parallel during the middle period.



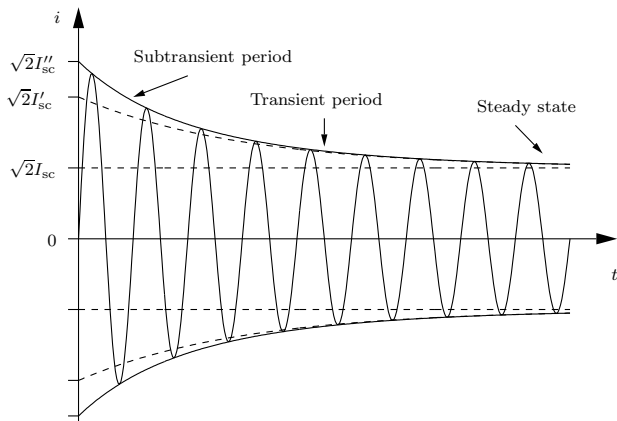
- The total reactance is called **transient reactance**:

$$X'_d = X_l + \frac{1}{1/X_a + 1/X_f} \quad (3.2)$$

- At a later stage X_f dies out and becomes open circuited, returning to the synchronous reactance X_d .

- Obviously $X_d'' < X_d' < X_d$.
- The machine thus offers a time-varying reactance which changes from X_d'' to X_d' and finally to X_d .
- Equivalently, during the short-circuit, the current will be split into three different periods:
 - the steady-state current I_{sc} ,
 - the transient current I_{sc}' , and
 - the initial sub-transient current I_{sc}'' .
- The initial **sub-transient period** when the current is large as the machine offers sub-transient reactance
- The middle **transient period** where the machine offers transient reactance
- The **steady-state period** when the machine offers synchronous reactance

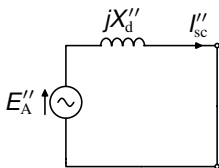
3 Synchronous reactance during short circuit



$$i(t) = (I''_{sc} - I'_{sc})e^{-\frac{t}{T''_d}} + (I'_{sc} - I_{sc})e^{-\frac{t}{T'_d}} + I_{sc}$$

where T''_d and T'_d are the machine transient and sub-transient time constants

- In the power flow analysis we are modeling generators as constant power sources (PV characteristics)
- This model is no longer valid during the short-circuit since the power and voltage regulators operate with much larger time constants.
- The modified model below is thus used:

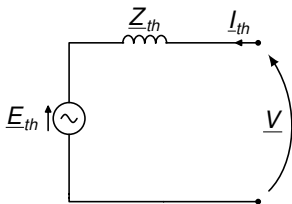


- E''_A represents the sub-transient electromagnetic field and X''_d the sub-transient reactance of generators or the internal grid impedance of a feeder
- This model may also be applied to large motor loads.

- 1 Overview
- 2 Fault at RL circuit
- 3 Short circuit of a synchronous machine
- 4 Short-circuit capacity (or fault level)**
- 5 Algorithms for short circuit studies

Thévenin's theorem: Seen from one port, a linear circuit can be replaced by an equivalent composed of:

- a voltage source (\underline{E}_{th}) = voltage that appears at the port when it is opened
- in series with an impedance ($\underline{Z}_{th} = R_{th} + jX_{th}$) = impedance seen from the port after having removed all sources. For high-voltage systems, R_{th} is frequently ignored.



- Based on the above theorem, when analyzing any load or distribution network connected through one bus to the network, we can represent the higher network with a Thévenin equivalent circuit to simplify the calculations.

- The **short-circuit capacity (or fault level)** characterizes both the severity of a fault and the network "strength" at a given bus

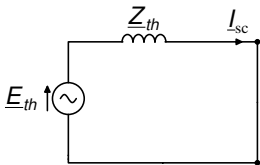
$$S_{sc} = \sqrt{3}V_L I_{sc}$$

where V_L is the line voltage at the bus of the fault and I_{sc} the current in each line of a zero-impedance three-phase short-circuit at the bus

- Used to size circuit breakers
 - the larger I_{sc} , the more "powerful" the breaker must be to interrupt the electric arc
 - the larger V_L , the larger the recovery voltage at the terminals of the breaker.

4 Relation between short-circuit capacity and Thévenin equivalent

If we want to analyze the short-circuit capacity at a bus where the rest of the network is represented by a Thévenin equivalent.



- The short circuit current is given by

$$|I_{sc}| = \left| \frac{E_{th}}{\sqrt{3}Z_{th}} \right| = \left| \frac{V_L}{\sqrt{3}Z_{th}} \right|$$


- The short-circuit capacity is given by

$$S_{sc} = \frac{V_L^2}{|Z_{th}|}$$


- Or, in per-unit with $V_B = V_L$:

$$S_{sc}^{pu} = \frac{S_{sc}}{S_B} = \frac{V_L^2}{S_B |Z_{th}|} = \frac{V_B^2}{S_B} \frac{1}{|Z_{th}|} = \frac{Z_B}{|Z_{th}|} = \frac{1}{|Z_{th}^{pu}|}$$

- The larger S_{sc} , the smaller $|\underline{Z}_{th}|$ and, hence, the smaller the variations of voltage $|\underline{V}_L|$ with changes of the reactive power Q
- Largely fluctuating loads must be connected to buses with large enough S_{sc} values
- As $|\underline{Z}_{th}| \rightarrow 0$: infinite bus

 A large S_{sc} requires to install circuit breakers with a strong current interruption capability but it also means that the voltage is better supported when facing disturbances in the network.

- 1 Overview
- 2 Fault at RL circuit
- 3 Short circuit of a synchronous machine
- 4 Short-circuit capacity (or fault level)
- 5 **Algorithms for short circuit studies**
 - Fault analysis using nodal equations
 - Faulted networks containing balanced voltage sources only

 **Definition:** the net response caused by two or more stimuli is the sum of the responses that would have been caused by each stimulus individually

Assumptions:

- All line capacitances are ignored.
- Synchronous generators and motors are modeled with the sub-transient model (see 29)
- all loads are represented by constant shunt admittances. Constant power loads are replaced by:

$$Y_L = \frac{P_L - jQ_L}{V_L^2}$$

5.1 Short-circuit nodal equations

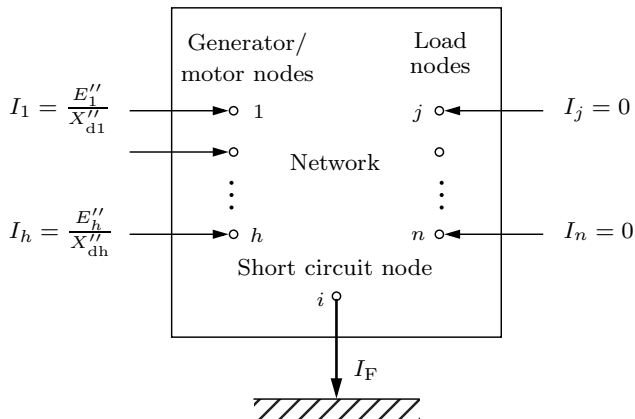
In order to calculate the short-circuit current I_F at node i , we can use the nodal equations

$$\mathbf{Y} \cdot \mathbf{V} = \mathbf{I}$$

- \mathbf{Y} is the nodal admittance matrix extended to incorporate the generator sub-transient reactances (X_d'').
- \mathbf{V} is the nodal voltage vector
- \mathbf{I} is the vector of injection currents

$$I_j = \begin{cases} \frac{E_j''}{X_{dj}''} & \text{for generator nodes} \\ 0 & \text{for load nodes} \\ -I_F & \text{for short-circuit node without generation} \\ -I_F + \frac{E_j''}{X_{dj}''} & \text{for short-circuit node with generation} \end{cases}$$

5.1 Short-circuit nodal equations



- Using the symmetrical component theory, the three-phase power system is decomposed in three decoupled networks: positive, negative, and zero-sequence networks.
- Respectively, the fault current is split into I_F^1 , I_F^2 , and I_F^0 .
- Leading to the nodal equations

$$\mathbf{Y}^1 \cdot \mathbf{V}^1 = \mathbf{I}^1, \quad \mathbf{Y}^2 \cdot \mathbf{V}^2 = \mathbf{I}^2, \quad \mathbf{Y}^0 \cdot \mathbf{V}^0 = \mathbf{I}^0$$

- Assuming a fault on node i and no negative or zero-sequence current injections:

$$\mathbf{Y}^1 \cdot \begin{pmatrix} \mathbf{V}_{\neq i}^1 \\ V_i^1 \end{pmatrix} = \begin{pmatrix} \mathbf{I}_{\neq i}^1 \\ -I_F^1 \end{pmatrix}, \quad \mathbf{Y}^2 \cdot \begin{pmatrix} \mathbf{V}_{\neq i}^2 \\ V_i^2 \end{pmatrix} = \begin{pmatrix} \mathbf{0} \\ -I_F^2 \end{pmatrix}, \quad \mathbf{Y}^0 \cdot \begin{pmatrix} \mathbf{V}_{\neq i}^0 \\ V_i^0 \end{pmatrix} = \begin{pmatrix} \mathbf{0} \\ -I_F^0 \end{pmatrix}$$

- Using the superposition theorem, we can split the nodal equations of the positive sequence to the pre-fault and post-fault:

$$\mathbf{Y}^1 \cdot \mathbf{V}_{pre}^1 = \begin{pmatrix} I_{\neq i}^1 \\ 0 \end{pmatrix} \quad \mathbf{Y}^1 \cdot \mathbf{V}_{post}^1 = \begin{pmatrix} I_{\neq i}^1 \\ -I_F^1 \end{pmatrix}$$

- Leading to

$$\mathbf{Y}^1 \cdot \Delta \mathbf{V}^1 = \begin{pmatrix} \mathbf{0} \\ -I_F^1 \end{pmatrix}$$

where $\Delta \mathbf{V}^1 = \mathbf{V}_{post}^1 - \mathbf{V}_{pre}^1$

- Similarly for all symmetrical components:

$$\Delta \mathbf{V}^1 = \mathbf{Z}^1 \begin{pmatrix} \mathbf{0} \\ -I_F^1 \end{pmatrix}, \quad \Delta \mathbf{V}^2 = \mathbf{Z}^2 \begin{pmatrix} \mathbf{0} \\ -I_F^2 \end{pmatrix}, \quad \Delta \mathbf{V}^0 = \mathbf{Z}^0 \begin{pmatrix} \mathbf{0} \\ -I_F^0 \end{pmatrix}$$

where the **nodal impedance** matrices \mathbf{Z}^1 , \mathbf{Z}^2 and \mathbf{Z}^0 , are the inverse matrices of \mathbf{Y}^1 , \mathbf{Y}^2 and \mathbf{Y}^0 , respectively

The previous equation for positive sequence and a fault at node k can be expanded to

$$\begin{pmatrix} \Delta V_1^1 \\ \vdots \\ \Delta V_k^1 \\ \vdots \\ \Delta V_N^1 \end{pmatrix} = \begin{pmatrix} Z_{11}^1 & \cdots & Z_{1k}^1 & \cdots & Z_{1N}^1 \\ \vdots & \ddots & \vdots & \ddots & \vdots \\ Z_{k1}^1 & \cdots & Z_{kk}^1 & \cdots & Z_{kN}^1 \\ \vdots & \ddots & \vdots & \ddots & \vdots \\ Z_{N1}^1 & \cdots & Z_{Nk}^1 & \cdots & Z_{NN}^1 \end{pmatrix} \begin{pmatrix} 0 \\ \vdots \\ -I_F^1 \\ \vdots \\ 0 \end{pmatrix} = \begin{pmatrix} -Z_{1k}^1 I_F^1 \\ \vdots \\ -Z_{kk}^1 I_F^1 \\ \vdots \\ -Z_{Nk}^1 I_F^1 \end{pmatrix} \quad (5.1)$$

Which leads to:

$$\Delta V_k^1 = -Z_{kk}^1 I_F^1$$

and similarly for the negative and zero sequence:

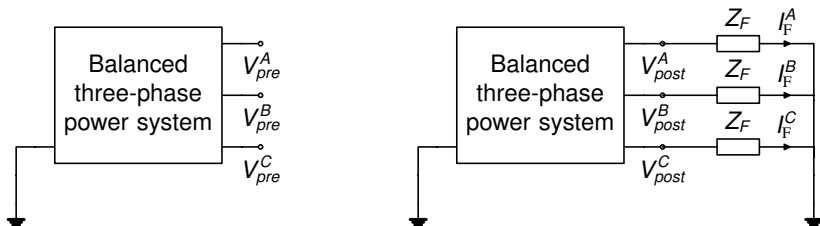
$$\Delta V_k^2 = -Z_{kk}^2 I_F^2, \quad \Delta V_k^0 = -Z_{kk}^0 I_F^0$$

Expanding when the pre-fault voltages are symmetrical (i.e.,

$$V_{k,pre}^2 = V_{k,pre}^0 = 0):$$

$$\boxed{V_{k,post}^1 = V_{k,pre}^1 - Z_{kk}^1 I_F^1, \quad V_{k,post}^2 = -Z_{kk}^2 I_F^2, \quad V_{k,post}^0 = -Z_{kk}^0 I_F^0} \quad (5.2)$$

If we have a three-phase short circuit of impedance Z_F , the diagrams before and after the fault can be depicted as:



The voltages and currents post-fault are:

$$V_{post}^A = Z_F I_F^A, \quad V_{post}^B = Z_F I_F^B, \quad V_{post}^C = Z_F I_F^C, \quad I_F^A + I_F^B + I_F^C = 0$$

The symmetrical components are then:

$$V_{post}^1 = V_{post}^A, \quad I_F^1 = I_F^A, \quad V_{post}^2 = V_{post}^0 = I_F^2 = I_F^0 = 0$$

5.2 Balanced three-phase to earth short-circuit fault

Assuming the short-circuit is on bus k and combining with (5.2) leads to:

$$\boxed{I_F^1 = \frac{-V_{k,pre}^1}{Z_{kk}^1 + Z_F}, \quad I_F^2 = 0, \quad I_F^0 = 0} \quad (5.3)$$

and:

$$I_F^A = I_F^1, \quad I_F^B = \alpha I_F^A, \quad I_F^C = \alpha^2 I_F^A$$

We can then compute the post-fault voltages from (5.1) as:


$$\begin{pmatrix} V_{1,post}^1 \\ \vdots \\ V_{k,post}^1 \\ \vdots \\ V_{N,post}^1 \end{pmatrix} = \begin{pmatrix} V_{1,pre}^1 - Z_{1k}^1 I_F^1 \\ \vdots \\ V_{k,pre}^1 - Z_{kk}^1 I_F^1 \\ \vdots \\ V_{N,pre}^1 - Z_{Nk}^1 I_F^1 \end{pmatrix} \quad (5.4)$$

and line currents:

$$\boxed{I_{jk,post}^1 = \frac{V_{j,post}^1 - V_{k,post}^1}{Z_{jk}^1}}$$

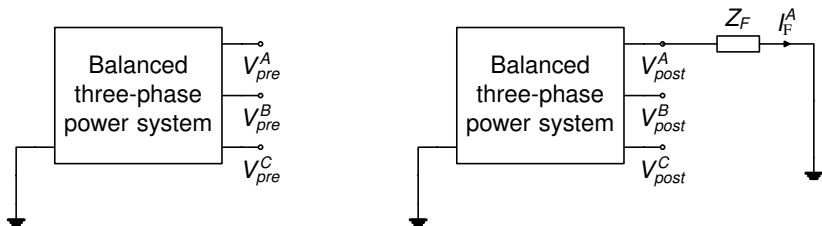
Steps to compute the three-phase to earth short-circuit fault:

- 1 Run a power-flow and compute the pre-fault voltages on every node.
- 2 Convert all power loads into their equivalent impedances (Y_L)
- 3 Convert all generators and motors to their Norton equivalent using the sub-transient reactances
- 4 Formulate the positive sequence nodal admittance matrix Y^1
- 5 Compute the nodal impedance matrix $Z^1 = (Y^1)^{-1}$
- 6 Compute the fault current using (5.3)
- 7 Compute the post-fault voltages using (5.4)

 A three-phase short-circuit clear of earth can be expressed to a three-phase to earth short-circuit fault using a Δ -Y transformation of the impedances Z_F .

5.2 Single-phase to earth short-circuit fault

If we have a single-phase to ground short-circuit of impedance Z_F , the diagrams before and after the fault can be depicted as:



The voltages and currents post-fault are:

$$V_{post}^A = Z_F I_F^A, \quad I_F^B = I_F^C = 0$$

The symmetrical components are then:

$$I_F^1 = I_F^2 = I_F^0 = \frac{1}{3} I_F^A$$

Combining the equations in the previous slide and (5.2), we get:

$$\begin{aligned}V_{post}^A &= 3Z_F I_F^1 = V_{post}^1 + V_{post}^2 + V_{post}^0 \\&= V_{pre}^1 - Z_{kk}^1 I_F^1 - Z_{kk}^2 I_F^2 - Z_{kk}^0 I_F^0 \\&= V_{pre}^1 - (Z_{kk}^1 + Z_{kk}^2 + Z_{kk}^0) I_F^1\end{aligned}$$

Leading to the currents:

$$I_F^1 = I_F^2 = I_F^0 = \frac{-V_{k,pre}^1}{Z_{kk}^1 + Z_{kk}^2 + Z_{kk}^0 + 3Z_F} \quad (5.5)$$

Giving:

$$I_F^A = 3I_F^1, \quad V_{post}^A = 3Z_F I_F^1 \quad (5.6)$$

5.2 Single-phase to earth short-circuit fault

We can compute the rest of the voltages similar as in (5.4):

$$\begin{pmatrix} V_{1,post}^1 \\ \vdots \\ V_{k,post}^1 \\ \vdots \\ V_{N,post}^1 \end{pmatrix} = \begin{pmatrix} V_{1,pre}^1 - Z_{1k}^1 I_F^1 \\ \vdots \\ V_{k,pre}^1 - Z_{kk}^1 I_F^1 \\ \vdots \\ V_{N,pre}^1 - Z_{Nk}^1 I_F^1 \end{pmatrix} \tag{5.7}$$

$$\begin{pmatrix} V_{1,post}^2 \\ \vdots \\ V_{k,post}^2 \\ \vdots \\ V_{N,post}^2 \end{pmatrix} = \begin{pmatrix} -Z_{1k}^2 I_F^2 \\ \vdots \\ -Z_{kk}^2 I_F^2 \\ \vdots \\ -Z_{Nk}^2 I_F^2 \end{pmatrix}, \quad \begin{pmatrix} V_{1,post}^0 \\ \vdots \\ V_{k,post}^0 \\ \vdots \\ V_{N,post}^0 \end{pmatrix} = \begin{pmatrix} -Z_{1k}^0 I_F^0 \\ \vdots \\ -Z_{kk}^0 I_F^0 \\ \vdots \\ -Z_{Nk}^0 I_F^0 \end{pmatrix}$$

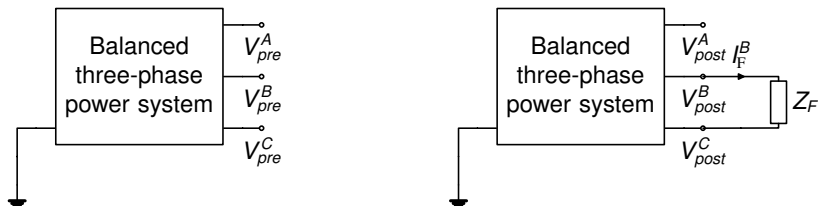
and line currents:

$$I_{jk,post}^1 = \frac{V_{j,post}^1 - V_{k,post}^1}{Z_{jk}^1}, \quad I_{jk,post}^2 = \frac{V_{j,post}^2 - V_{k,post}^2}{Z_{jk}^2}, \quad I_{jk,post}^0 = \frac{V_{j,post}^0 - V_{k,post}^0}{Z_{jk}^0}$$

Steps to compute the single-phase to earth short-circuit fault:

- 1 Run a power-flow and compute the pre-fault voltages on every node.
- 2 Convert all power loads into their equivalent impedances (Y_L)
- 3 Convert all generators and motors to their Norton equivalent using the sub-transient reactances
- 4 Formulate the positive sequence nodal admittance matrices Y^1 , Y^2 , and Y^0
- 5 Compute the nodal impedance matrices $Z^1 = (Y^1)^{-1}$, $Z^2 = (Y^2)^{-1}$, and $Z^0 = (Y^0)^{-1}$
- 6 Compute the fault current using (5.5)-(5.6)
- 7 Compute the post-fault voltages using (5.7)

If we have a two-phase short-circuit of impedance Z_F , the diagrams before and after the fault can be depicted as:



The voltages and currents post-fault are:

$$V_{post}^B - V_{post}^C = Z_F I_F^B, \quad I_F^B = -I_F^C, \quad I_F^A = 0$$

The symmetrical components are then:

$$I_F^1 = -I_F^2, \quad I_F^0 = 0 \xrightarrow{(5.2)} V_{post}^0 = 0$$

Which leads to:

$$\begin{aligned}V_{post}^B - V_{post}^C &= (\alpha^2 V_{post}^1 + \alpha V_{post}^2) - (\alpha V_{post}^1 + \alpha^2 V_{post}^2) \\ &= (\alpha^2 - \alpha)(V_{post}^1 - V_{post}^2) \\ Z_F I_F^B &= Z_F(\alpha^2 I_F^1 + \alpha I_F^2) = Z_F(\alpha^2 - \alpha)I_F^1\end{aligned}$$

Equating the two:

$$V_{post}^1 - V_{post}^2 = Z_F I_F^1$$

Combining with (5.2) for a fault at bus k :

$$V_{k,pre}^1 - Z_{kk}^1 I_F^1 + Z_{kk}^2 I_F^2 = V_{pre}^1 - Z_{kk}^1 I_F^1 - Z_{kk}^2 I_F^1 = Z_F I_F^1$$

Leading to:

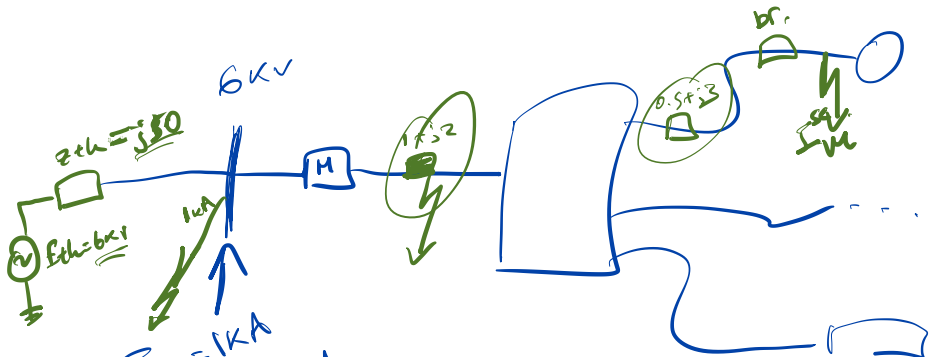
$$I_F^1 = -I_F^2 = \frac{V_{k,pre}^1}{Z_{kk}^1 + Z_{kk}^2 + Z_F} \quad (5.8)$$

The phase currents are then:

$$I_F^B = -I_F^C = (\alpha^2 - \alpha)I_F^1 = -j\sqrt{3}I_F^1 \quad (5.9)$$

Steps to compute the Phase-to-phase short-circuit fault:

- 1 Run a power-flow and compute the pre-fault voltages on every node.
- 2 Convert all power loads into their equivalent impedances (Y_L)
- 3 Convert all generators and motors to their Norton equivalent using the sub-transient reactances
- 4 Formulate the positive sequence nodal admittance matrices \mathbf{Y}^1 , \mathbf{Y}^2
- 5 Compute the nodal impedance matrices $\mathbf{Z}^1 = (\mathbf{Y}^1)^{-1}$, $\mathbf{Z}^2 = (\mathbf{Y}^2)^{-1}$
- 6 Compute the fault current using (5.8)-(5.9)
- 7 Compute the post-fault voltages using (5.7)



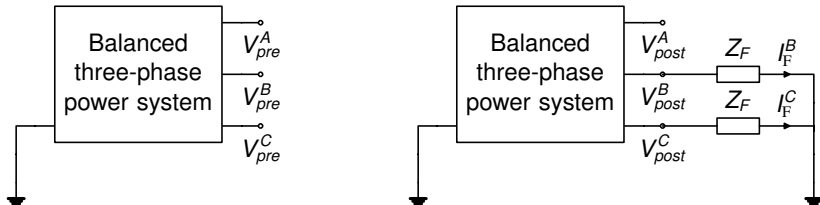
$$I_{sc} = 1kA$$

$$S_{sc} = 200MVA$$

$$= \sqrt{3} \cdot 6kV \cdot I_{sc} = 200MVA$$

$$I_{sc} = \frac{6kV}{Z_{th}}$$

If we have a two-phase short circuit of impedance Z_F to earth, the diagrams before and after the fault can be depicted as:



The voltages and currents post-fault are:

$$V_{post}^B = Z_F I_F^B, \quad V_{post}^C = Z_F I_F^C, \quad I_F^A = 0$$

The symmetrical components are then:

$$I_F^1 + I_F^2 + I_F^0 = 0 \rightarrow I_F^0 = -(I_F^1 + I_F^2)$$

5.2 Two-phase to earth short-circuit fault

We expand for short-circuit on bus k :

$$V_{post}^B - V_{post}^C = Z_F(I_F^B - I_F^C) \Rightarrow$$

$$V_{k,pre}^1 - (Z_{kk}^1 + Z_F)I_F^1 = -(Z_{kk}^2 + Z_F)I_F^2$$

and:

$$V_{post}^B + V_{post}^C = Z_F(I_F^B + I_F^C) \Rightarrow$$

$$-(Z_F + Z_{kk}^2)I_F^2 = -(Z_F + Z_{kk}^0)I_F^0 = V_{k,pre}^1 - (Z_{kk}^1 + Z_F)I_F^1$$

After many calculations¹:

$$I_F^1 = \frac{V_{k,pre}^1[(Z_{kk}^0 + Z_F) + (Z_{kk}^2 + Z_F)]}{(Z_{kk}^1 + Z_F)(Z_{kk}^2 + Z_F) + (Z_{kk}^2 + Z_F)(Z_{kk}^0 + Z_F) + (Z_{kk}^1 + Z_F)(Z_{kk}^0 + Z_F)}$$

$$I_F^2 = \frac{-V_{k,pre}^1(Z_{kk}^0 + Z_F)}{(Z_{kk}^1 + Z_F)(Z_{kk}^2 + Z_F) + (Z_{kk}^2 + Z_F)(Z_{kk}^0 + Z_F) + (Z_{kk}^1 + Z_F)(Z_{kk}^0 + Z_F)}$$

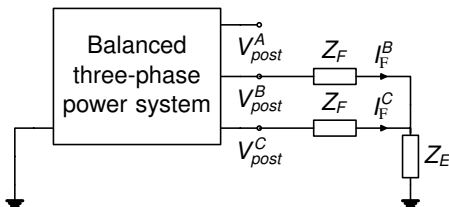
$$I_F^0 = \frac{-V_{k,pre}^1(Z_{kk}^2 + Z_F)}{(Z_{kk}^1 + Z_F)(Z_{kk}^2 + Z_F) + (Z_{kk}^2 + Z_F)(Z_{kk}^0 + Z_F) + (Z_{kk}^1 + Z_F)(Z_{kk}^0 + Z_F)}$$

¹See Chapter 2 of N. Tleis, Power Systems Modelling and Fault Analysis: Theory and Practice, 2nd ed. Academic Press, 2019.

The post-fault voltages can be computed by (5.7) and the total current to ground as:

$$I_F = I_F^B + I_F^C = 3I_F^0$$

📖 If an earth impedance Z_E is present in the common connection to earth as shown below, the zero sequence impedance should be updated to $Z_{kk}^0 + 3Z_E$.



Steps to compute the single-phase to earth short-circuit fault:

- 1 Run a power-flow and compute the pre-fault voltages on every node.
- 2 Convert all power loads into their equivalent impedances (Y_L)
- 3 Convert all generators and motors to their Norton equivalent using the sub-transient reactances
- 4 Formulate the positive sequence nodal admittance matrices \mathbf{Y}^1 , \mathbf{Y}^2 , and \mathbf{Y}^0
- 5 Compute the nodal impedance matrices $\mathbf{Z}^1 = (\mathbf{Y}^1)^{-1}$, $\mathbf{Z}^2 = (\mathbf{Y}^2)^{-1}$, and $\mathbf{Z}^0 = (\mathbf{Y}^0)^{-1}$
- 6 Compute the fault current using the equations in Slide 53
- 7 Compute the post-fault voltages using (5.7)