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STEPSS has been developed by Dr Petros Aristidou (email: petros.aristidou@cut.ac.cy) and Dr Thierry Van Cutsem (email: thierry.h.van.cutsem@gmail.com), hereafter referred to as the "Authors".

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This free-of-charge version of the software is limited to power system models up to 1000 buses (or nodes) and can be executed with parallelization using no more than two cores. For extensions to larger models or execution using more than two cores, you have to contact the Authors